



PATENT 5500-98000/TT5540

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

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Application No.: 10/822,506

Filed: April 12, 2004

Inventors:

John V. Sell

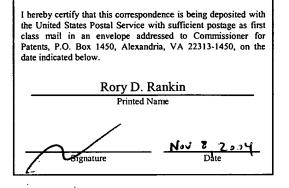
Title: CPU AND GRAPHICS

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Examiner: Unknown Group/Art Unit: 2173

Atty. Dkt. No: 5500-98000



INFORMATION DISCLOSURE STATEMENT

Commissioner for Patents P.O. Box 1450 Alexandria, VA 22313-1450

Sir:

Applicant requests consideration of \boxtimes the references listed on the attached Form PTO-1449 and/or \boxtimes the additional information identified below in paragraph 3.

- A copy of each required reference listed on the Form PTO-1449 is enclosed.
- The references listed on the Form PTO-1449 were previously cited in application serial no. _______ to which the captioned application is seeking priority under 35 U.S.C. §120; therefore, a copy of each reference is not enclosed.
- The present application is a U.S. national patent application filed after June 30, 2003, or an international application that entered the national stage under 35 USC § 371 after June 30, 2003. Therefore, copies of the cited U.S. patents and/or U.S. patent application publications are not enclosed.
- 1. This Information Disclosure Statement is submitted:

	a.		within 3 months of the filing date of a national application other than a continued prosecution application under § 1.53(d); within 3 months of the date of entry of the national stage as set forth in § 1.491 in an International application; before the mailing date of a first Office Action on the merits; or before the mailing of a first Office Action after the filing of a request for							
	b.		continued examination under § 1.114. after the events of above paragraph 1a and prior to the mailing date of a final Office Action or Notice of Allowance, and thus: the certification of paragraph 2 below is provided, or a fee of \$180.00 is enclosed.							
	c.		after the mailing date of a final Office Action or a Notice of Allowance and prior to payment of the issue fee, and thus: the certification of paragraph 2 below is provided and a fee of \$180.00 is enclosed.							
2.	It is he	reby cert	ified:							
		that each item of information contained in this Information Disclosure Statement was cited in a communication from a foreign patent office in a counterpart application and that this communication was not received by any individual designated in § 1.56(c) more than thirty (30) days prior to the filing of the information disclosure statement;								
		that each item of information contained in this Information Disclosure Statement was cited in a communication from a foreign patent office in a counterpart foreign application not more than three (3) months prior to the filing of the Statement; or								
		was cit foreign after m	item of information contained in the Information Disclosure Statement ted in a communication from a foreign patent office in a counterpart application or, to the knowledge of the person signing the certification aking reasonable inquiry, was known to any individual designated in § more than three (3) months prior to the filing of the Statement.							
3.			eration of the following additional information (including any co-pending doned U.S. applications, prior uses and/or sales, etc.) is requested:							
			U.S. Patent Application Serial No. 10/676,625							
	U.S. Patent Application Serial No. 10/090,507									
4.	For eac	For each non-English language reference listed on the attached Form PTO-1449:								
		referen	ce is made to an English language translation submitted herewith, and/or							
			ce is made to a foreign patent office search report (in the English ge) submitted herewith, and/or							

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		reference is made to an English language translation of a foreign patent office search report submitted herewith, and/or
		reference is made to the concise explanation contained in the specification of the present application at page(s), and/or
		reference is made to the concise explanation set forth below:
5.		Applicant also offers the following comments for the Examiner's consideration:
6.		Also enclosed is a copy of a foreign search report citing these references.
7.		The listed documents were brought to the attention of the Applicant(s) after payment of the issue fee in the captioned case. The documents were cited in a communication from a foreign patent office in a counterpart foreign application not more than three months prior to the filing of this Information Disclosure Statement. Applicant(s) request this Information Disclosure Statement and attached Form PTO-1449 be placed in the file of the captioned application.
8.		Applicant(s) requests that the Information Disclosure Statement and attached Form PTO-1449 and references, which are being filed before the grant of the patent and pursuant to 37 C.F.R. § 1.97(i), be placed in the file of the captioned application.
	If any	required fees are missing, the Commissioner is authorized to charge said fees to
-		ood, Kivlin, Kowert & Goetzel, P.C. Deposit Account No. 501505/5500-
98000	/RDR.	
		Respectfully submitted,
		Rory D. Rankin Reg. No. 47,884 Attorney for Applicant(s)
KO P. O. I Austin	WERT 8 Box 398 , Texas 3 853-8800	F, HOOD, KIVLIN, & GOETZEL, P.C. 78767
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Form PTO-1449 (modified)

ATTY. DKT. NO. 5500-98000

SERIAL NO. 10/822,506

List of Patents and Publications For Applicant's Information
Disclosure Statement

APPLICANT: John V. Sell

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GROUP: 2173

Use several sheets if necessary)			FILING DA	TE: April 12, 2004						
	_	Ţ	J.S. PATENT	DOCUMENTS						
EXAM. INITIALS	REF. DES.	DOCUMENT NUMBER	DATE	NAME	CLASS	SUB CLASS	FILING DATE APPROPRIAT			
		5,949,423	9/7/99	Olsen						
		6,226,012	5/1/01	Priem, et al.						
		6,239,808	5/29/01	Kirk, et al.						
		6,266,064	7/24/01	Snyder						
		FOR	EIGN PATE	NT DOCUMENTS						
EXAM. INITIALS	REF. DES.	DOCUMENT NUMBER	DATE	COUNTRY	CLASS	SUB CLASS	TRANSLATIO YES/NO			
	<u></u>	OTHER ART (Including Autho	or, Title, Date, Pertinent P	ages, Etc.)					
	A1 Thomas Pabst, "High-Tech and Vertex Juggling - NVIDIA's New GeForce3 GPU," February 27. 2001, pages.									
	A2	Alekey Berillo, "S3TC and FXT1 Texture Compression," Copyright by Digit-Life.com, 1997-2003, 21 pages.								
	A3	"Geode™ GX1 Processor Series Low Power Integrated x86 Solution," National Semiconductor Corporation, June 2002, 246 pages.								
	A4	Ned Greene, Michael Kass, Gavin Miller, "Hierarchical Z-Buffer Visibility," Siggraph Conference Proceedings, August 1993.								

EXAMINER:

DATE CONSIDERED:

EXAMINER: Initial if citation considered, whether or not citation is in conformance with MPEP 609; Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to the patent own